

Pune Vidyarthi Griha's
College of Engineering & Shrikrushna S. Dhamankar Institute of Management, Nashik

TIME TABLE

Department: Electronics and Telecommunication Engineering
Academic Year: 2026-27

Semester: I / II

TE w.e.f: 03/08/2026
R1

Time Day	Class	09:00 am - 10:00 am	10:00 am - 11:00 am	11:00 am - 11:15 am	11:15 am - 12:15 pm	12:15 pm - 01:15 pm	01:15 pm - 02:00 pm	02:00 pm - 03:00 pm	03:00 pm - 04:00 pm	
Monday	TE	T1-CELN (CSA), T2-LIBRARY T3-MC (ASP), T4-ML (AMB)		S H O R T R E C E S S	CELN (CSA)	MC (ASP)	L U N C H B R E A K	VLSI (ARS)	IPR&CL (SNS)	
Tuesday	TE	T1-CN (VKJ), T2-CELN (CSA) T3-VLSI (ARS), T4-MC (ASP)			CELN (CSA)	VLSI (ARS)		MC (ASP)	IPR&CL (SNS)	
Wednesday	TE	T1-ML (AMB), T2-CN (VKJ) T3-CELN (CSA), T4- VLSI (ARS)			MC (ASP)	VLSI (ARS)		CN (VKJ) / CC (SAB)	ML (AMB)	
Thursday	TE	T1- VLSI (ARS), T2-MC (CSA) T3-LIBRARY, T4-CC (SAB)			TECH. SEMINAR (ASP)	CELN (CSA)		CN (VKJ) / CC (SAB)	ML (AMB)	
Friday	TE	TECH. SEMINAR (ASP)	CN (VKJ) / CC (SAB)	T1-MC (CSA), T2-ML (AMB) T3-CC (SAB), T4-LIBRARY		T1-LIBRARY, T2-VLSI (ARS) T3-ML (AMB), T4-CELN (CSA)				
Saturday	TE	Extra/Guest/Remedial Session				Class Room No: 203, 204				