

Pune Vidyarthi Griha's
College of Engineering & S.S.D. Institute of Managment, Nashik - 4

CLASS TIME TABLE

Department: E&TC Engineering

CLASS:-SE

Academic Year: 2024-25

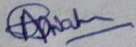
Semester: I / II

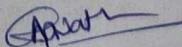
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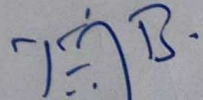
w.e.f : 01/07/2024

Time Day	Class	08.00 - 09.00am	09.00 - 10.00am	10 - 10.15am	10.15 - 11.15 am	11.15 - 12.15 pm	12.15- 01pm	01.00- 02.00pm	02.00 -03.00pm	
Monday	SE	M-III(PDH)	DS(SSG)	S H O R T R E C E S S	S1-DS(SSG), S2-ELEX.CKT(SNB),S3-DC(CSA), S4- EC.CKT(SMP)	L U N C H B R E A K	L U N C H B R E A K	S1-ESD(SAB),S2-DC(CSA), S3-EC.CKT(SMP), S4-M-III TUT(PDH)		
Tuesday	SE	EC.CKT(SMP)	M-III(PDH)		S1-EC.CKT(SMP),S2-M-III TUT(PDH), S3-DS(SSG), S4-DC(CSA)			DS(SSG)	ELEX.CKT(SNB)	
Wednesday	SE	ELEX.CKT(SNB)	M-III(PDH)		S1-ELEX.CKT(SNB), S2-EC.CKT(SMP), S3- M-III TUT(PDH), S4- ESD(ASP)			DC(CSA)	EC.CKT(SMP)	
Thursday	SE	DC(CSA)	EC.CKT(SMP)		S1-DC(ARS), S2-ESD(SAB), S3-ELEX.CKT(SNB), S4-DS(SSG)			LIBRARY HR.		
Friday	SE	M-III(PDH)	ELEX.CKT(SNB)		DS(SSG)			DC(CSA)	S1- M-III TUT(PDH), S2-DS(SSG), S3-ESD(SAB), S4-ELEX.CKT(SNB)	
Saturday	SE								Class room No 003 & 004	

Subjects		Faculty	
M-III	Engineering Mathematics II	PDH	Prof. P.D.Hase
Elex.C	Electronic Circuits	SNB	Prof.S.N.Bhangare
Elec.C	Electrical Circuits	SMP	Prof. S.M. Pangavhane
DC	Digital Circuits	ARS	Prof. A. R. Sonawane
		CSA	Prof. C. S.Ahire
DS	Data structures	SSG	Prof. S.S.Gore
ESD	Electronic Skill Development	SAB	Prof.S.A. Barahate
		ASP	Prof. A. S. Pawar


Prof. A. R. Sonawane
 Time Table I/C


Prof. A. R. Sonawane
 Academic Coordinator


Prof. Dr. M. V. Bhalerao
 HOD